IN THE CLAIMS:

Please cancel claims 1-47 and add new claims 48-96, as indicated in the complete listing of claims provided below.

1-47. (canceled)

48. (new) A data processing system, comprising:

means for storing an obfuscated stream, the obfuscated stream comprising parts which are interleaved, the parts having been taken from at least two operative instruction streams; and

means for executing the obfuscated stream;

wherein a second part of a second one of the at least two operative instruction streams is interleaved between two first parts of a first one of the at least two operative instruction streams; and

wherein when the two first parts are executed, the second part is also executed.

49. (new) The data processing system of claim 48, wherein the second part is stack balanced.

50. (new) The data processing system of claim 48, wherein the obfuscated stream further comprises an obfuscation code that interrelates the parts from the operative instruction streams.

51.   (new) The data processing system of claim 48, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

52.   (new) The data processing system of claim 48, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

53.   (new) The data processing system of claim 48, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

54.   (new) The data processing system of claim 48, wherein two of the operative instructions streams are the same.

55.   (new) A digital processing system, comprising:
memory to store an obfuscated stream, the obfuscated stream comprising parts which
    are interleaved, the parts having been taken from at least two operative
    instruction streams; and
a processor coupled with the memory, the processor to execute the obfuscated stream;
wherein a second part of a second one of the at least two operative instruction streams
    is interleaved between two first parts of a first one of the at least two operative
    instruction streams; and

wherein when the two first parts are executed, the second part is also executed.

56. (new) The digital processing system of claim 55, wherein the second part is stack balanced.

57. (new) The digital processing system of claim 55, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

58. (new) The digital processing system of claim 55, wherein the obfuscated stream further comprises an obfuscation code that interrelates the parts from the operative instruction streams.

59. (new) The digital processing system of claim 58, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

60. (new) The digital processing system of claim 55, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

61. (new) The digital processing system of claim 60, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

62. (new) The digital processing system of claim 55, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

63. (new) The digital processing system of claim 55, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

64. (new) The digital processing system of claim 63, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

65. (new) The digital processing system of claim 55, wherein two of the operative instructions streams are the same.

66. (new) The digital processing system of claim 65, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

67. (new) A server data processing system, comprising:
means for storing an obfuscated stream, the obfuscated stream comprising parts which are interleaved, the parts having been taken from at least two operative instruction streams;

means for transferring the obfuscated stream to a client data processing system

through a network for execution;

wherein a second part of a second one of the at least two operative instruction streams

is interleaved between two first parts of a first one of the at least two operative

instruction streams; and

wherein when the two first parts are executed, the second part is also executed.

68. (new) The server processing system of claim 67, wherein the second part is stack
balanced.

69. (new) The server processing system of claim 67, wherein the obfuscated stream
further comprises an obfuscation code that interrelates the parts from the operative
instruction streams.

70. (new) The server processing system of claim 67, wherein at least one of the parts has
been transformed before the parts are interleaved and after the parts are taken from
the operative instruction streams.

71. (new) The server processing system of claim 67, wherein at least one of the parts has
been so transformed before the parts are interleaved and after the parts are taken from
the operative instruction streams that the obfuscated stream performs at least the same
logical operations of one of the operative instruction streams.

72.   (new) The server processing system of claim 67, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

73.   (new) The server processing system of claim 67, wherein two of the operative instructions streams are the same.

74.   (new) A server digital processing system, comprising:

memory to store an obfuscated stream, the obfuscated stream comprising parts which are interleaved, the parts having been taken from at least two operative instruction streams;

a processor coupled with the memory; and

a communication device coupled with the processor, the communication device to communicate the obfuscated stream to a client data processing system through a network for execution;

wherein a second part of a second one of the at least two operative instruction streams is interleaved between two first parts of a first one of the at least two operative instruction streams; and

wherein when the two first parts are executed, the second part is also executed.

75.   (new) The server digital processing system of claim 74, wherein the second part is stack balanced.

76. (new) The server digital processing system of claim 74, wherein the obfuscated stream further comprises an obfuscation code that interrelates the parts from the operative instruction streams.

77. (new) The server digital processing system of claim 74, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

78. (new) The server digital processing system of claim 74, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

79. (new) The server digital processing system of claim 74, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

80. (new) The server digital processing system of claim 74, wherein two of the operative instructions streams are the same.

81. (new) The server digital processing system of claim 74, wherein the communication device comprises a network interface.

82.   (new) The server digital processing system of claim 74, wherein the network interface comprises an Ethernet interface.

83.   (new) A method, comprising:

storing an obfuscated stream, the obfuscated stream comprising parts which are interleaved, the parts having been taken from at least two operative instruction streams;

transferring the obfuscated stream to a client data processing system through a network;

wherein a second part of a second one of the at least two operative instruction streams is interleaved between two first parts of a first one of the at least two operative instruction streams; and

wherein when the two first parts are executed, the second part is also executed.

84.   (new) The method of claim 83, wherein the second part is stack balanced.

85.   (new) The method of claim 83, wherein the obfuscated stream further comprises an obfuscation code that interrelates the parts from the operative instruction streams.

86.   (new) The method of claim 83, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

87. (new) The method of claim 83, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

88. (new) The method of claim 83, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

89. (new) The method of claim 83, wherein two of the operative instructions streams are the same.

90. (new) A machine readable medium containing executable computer program instructions which when executed by a data processing system cause said system to perform a method, the method comprising:

storing an obfuscated stream, the obfuscated stream comprising parts which are
interleaved, the parts having been taken from at least two operative instruction streams;

downloading the obfuscated stream to a client data processing system through a
network;

wherein a second part of a second one of the at least two operative instruction streams
is interleaved between two first parts of a first one of the at least two operative
instruction streams; and

wherein when the two first parts are executed, the second part is also executed.

91.   (new) The medium of claim 90, wherein the second part is stack balanced.

92.   (new) The medium of claim 90, wherein the obfuscated stream further comprises an obfuscation code that interrelates the parts from the operative instruction streams.

93.   (new) The medium of claim 90, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

94.   (new) The medium of claim 90, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

95.   (new) The medium of claim 90, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

96.   (new) The medium of claim 90, wherein two of the operative instructions streams are the same.